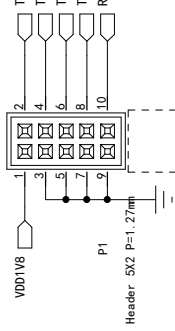


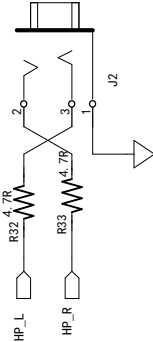
Debug Connect



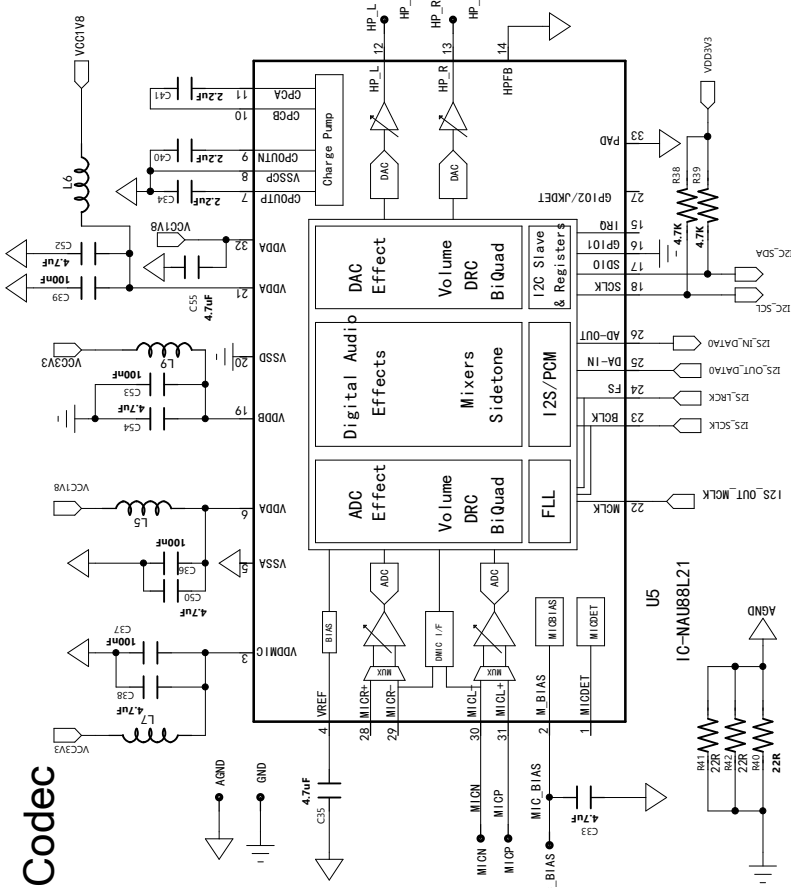
Note 2:

For easy connection with debugger, reserve space for 10x2 dual-row pin header with 1.27mm pitch for Debug Connect during PCB layout

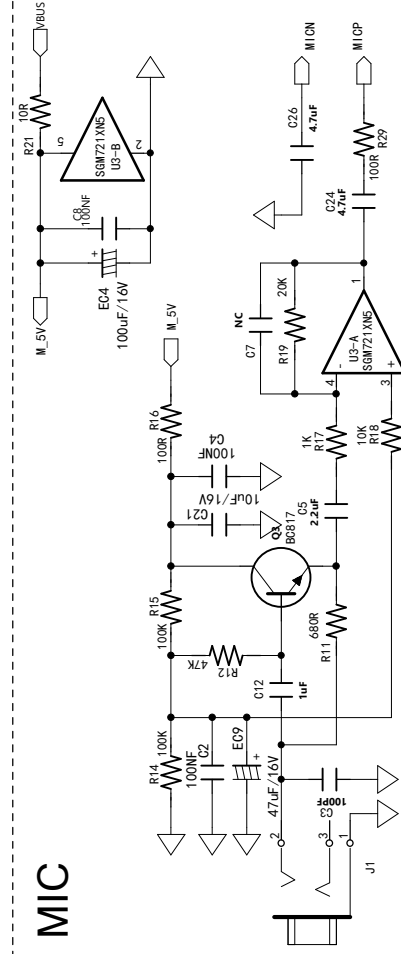
SPK



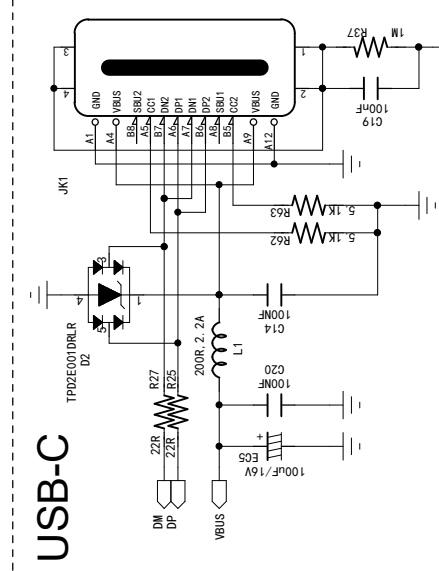
Codec



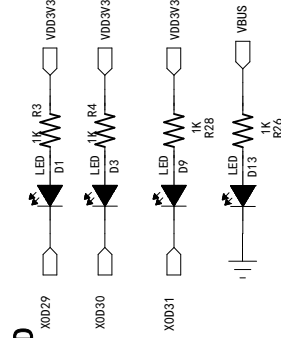
MIC



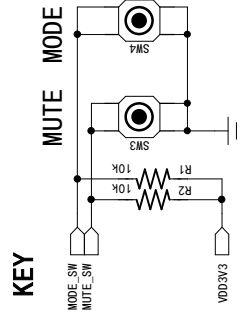
USB-C



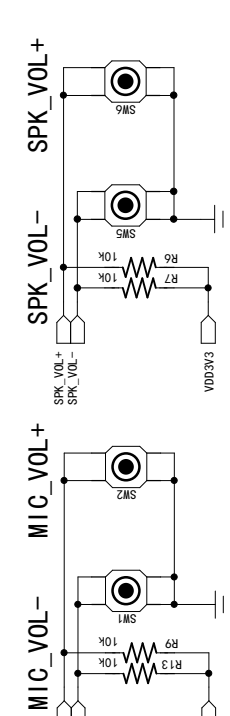
LED



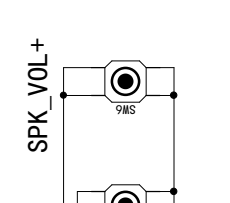
KEY



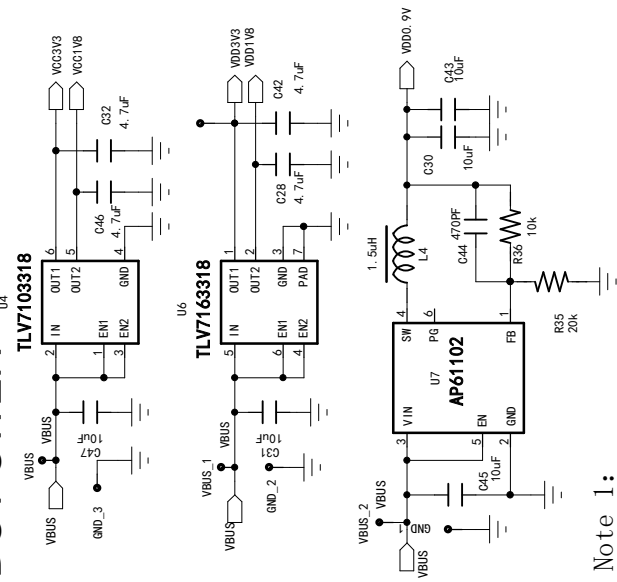
MIC_VOL+



SPK_VOL+



DC POWER



Note 1:

VDD3V3 must be valid before

VDD1V8 and VDD0.9V valid